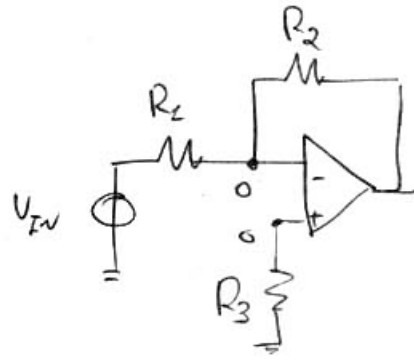


ESERCIZIO 1

1.A

$$|G|_{F.} = \frac{R_2}{R_1} = \boxed{-20}$$

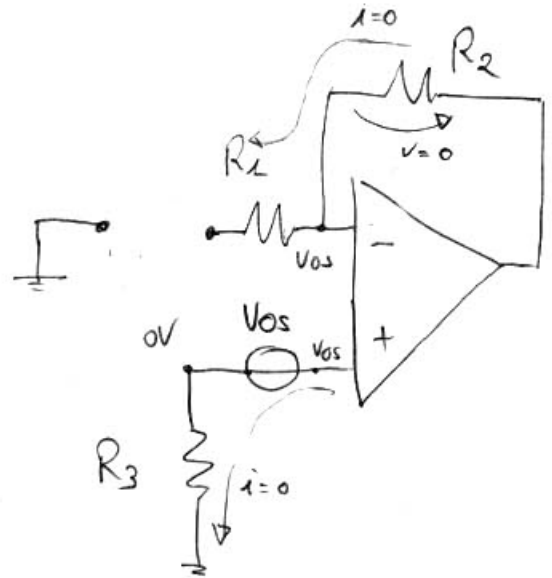


1.B

OFFSET TENSIONE COSTANTE →
 → CONDENSATO APERTI →
 → LO STADIO È UN BUFFER

$$|V_{OUT}| = V_{OS} = \boxed{\pm 12 \text{ mV}}$$

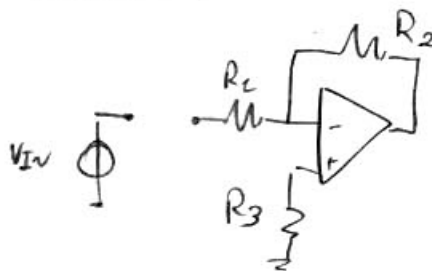
SEGNO NON NOTO
 A PRIORI



1.C

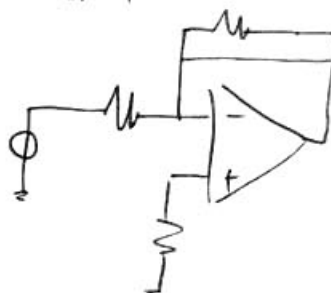
METODO 1

* A BASSA FREQ: I CONDENSATORI SONO APERTI:



$$G_{ID}(0) = 0$$

* AD ALTA FREQ: I CONDENSATORI SONO DEI CORTI



$$G_{ID}(\infty) = 0$$

⊗ A MEDIA FREQ :

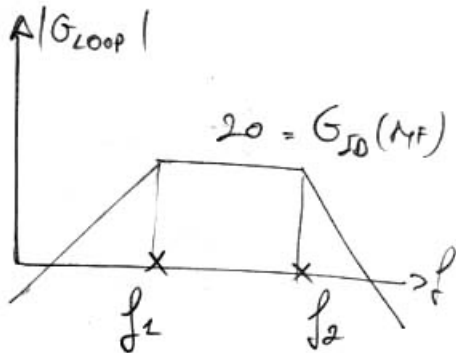
$$G_{JD}(MF) = -\frac{R_2}{R_1} \quad (\text{VEDI 1.A})$$

⊗ LO STADIO HA CERTAMENTE 2 POLI (2 CONDENSATORI), MA NON PUO' AVERE ZERI PERCHE' PUNTO DA 0 A DIFERENZA FREQ E ARRIVA A 0 AD ALTA FREQ

POLI :

$$\tau_1 = R_1 C_1 = 1k\Omega \times 470nF = 470\mu s \rightarrow |f_{p1}| = \frac{1}{2\pi\tau_1} = \boxed{338Hz}$$

$$\tau_2 = R_2 C_2 = 20k\Omega \times 10pF = 200ns \rightarrow |f_{p2}| = \frac{1}{2\pi\tau_2} = \boxed{796kHz}$$

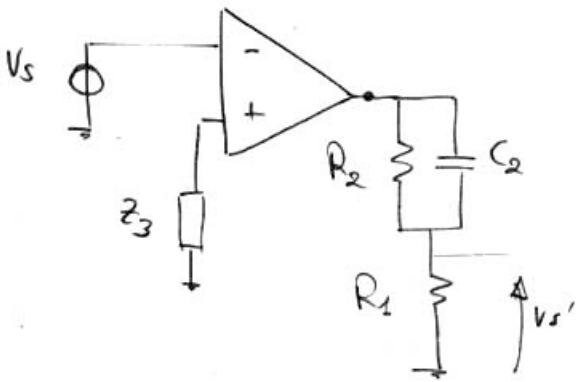


FILTRO PASSABANDA

METODO 2

$$|G_{JD}| = -\frac{z_2(s)}{z_1(s)} = -\frac{R_2 // \frac{1}{sC_2}}{R_1 + \frac{1}{sC_1}} = \dots = \boxed{-\frac{sC_1 R_2}{(1+sC_1 R_1)(1+sC_2 R_2)}}$$

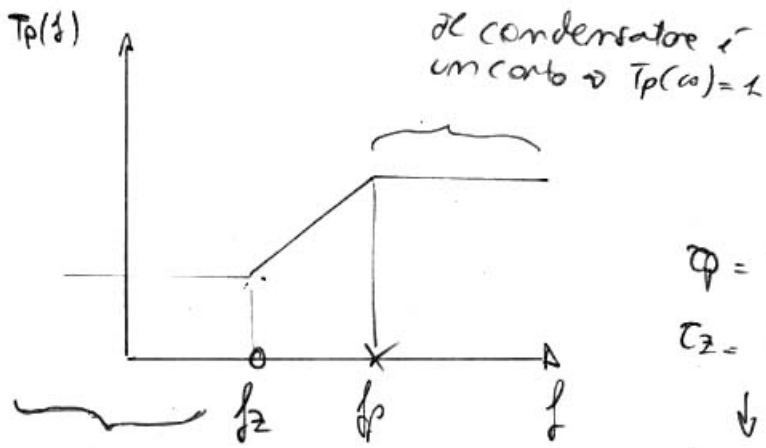
1.D



$$G_{Loop}(s) = -A(s) T_p(s) \quad \leftarrow \text{TRASFERIMENTO DEL PARTITORE IN USCITA}$$

$$= -A(s) \frac{R_1}{R_1 + (R_2 // \frac{1}{sC_2})} = \dots =$$

$$= -A(s) \frac{R_1}{R_1 + R_2} \frac{1 + sC_2 R_2}{1 + sC_2 (R_1 // R_2)}$$



il cond. è un
 aperto, quindi
 $T_p(0) = \frac{R_2}{R_1 + R_2} = \frac{1}{21}$

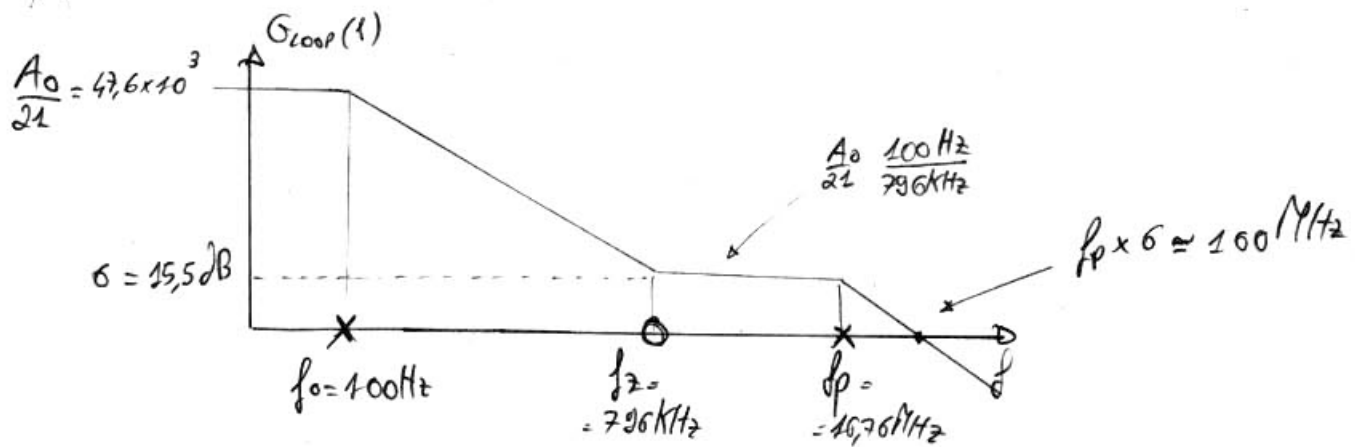
$$\tau_p = (R_1 // R_2) C = 9,5 \text{ ms}$$

$$\tau_z = C R_2 = 200 \text{ ns}$$

↓

$$f_p = \frac{1}{2\pi \tau_p} = 16,76 \text{ MHz}$$

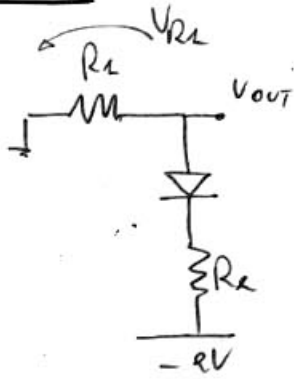
$$f_z = \frac{1}{2\pi \tau_z} = 796 \text{ kHz}$$



Il $G_{loop} = 1$ per $f = 100 \text{ MHz}$. Il circuito è ben compensato
 quando G_{loop} è sufficientemente grande, per esempio quando
 $G_{loop} = 5 \Rightarrow \left[f + \frac{100 \text{ MHz}}{5} = 20 \text{ MHz} \right]$

ESERCIZIO 2

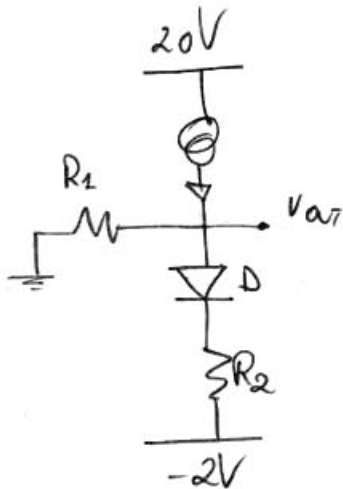
2.A



$$\boxed{V_{OUT}} - V_{R1} = \left(-\frac{R1}{R1+R2} \right) (2V - 0,7V) =$$

$$= -\frac{2,5k}{7,5k} (1,3V) = \boxed{-433mV}$$

2.B



⊗ $I_{IN} > 0$: Immetto corrente nel nodo
 $V_{OUT} \rightarrow V_{OUT} \text{ sale} \rightarrow$ il diodo resta
 comunque acceso

⊗ $I_{IN} < 0$: V_{OUT} scende \rightarrow il diodo può
 spegnersi, se $V_{OUT} < -4,3V$

DIODO ON

principio di sovrapposizione degli effetti: ai $-433mV$ si sovrappone l'effetto
 di I_{IN} iniettata in $R1 \parallel R2$:

$$V_{OUT} = -433mV + I_{IN} \times R1 \parallel R2$$

Valori di picco:

$$\boxed{V_{OUT|pk}} = -433mV + 6mA \times (2,5k \parallel 5k) = -433mV + 10V = \boxed{9,567V}$$

DIODO OFF

D si spegne quando $V_{OUT} = -4,3V$ corrispondente ad una corrente I_{IN} :

$$I_{IN} = \frac{V_{OUT} + 433mV}{R1 \parallel R2} = \frac{-4,3V + 0,433V}{2,5k \parallel 5k} = -522\mu A$$

Quando il diodo è spento tutta la corrente fluisce in $R1$:

$$V_{OUT} = -R1 I_{IN} \rightarrow \boxed{V_{OUT|pk}} = -2,5k \times 6mA = \boxed{-15V}$$

ESERCIZIO 3

3.4

$$\boxed{V_{Gm}} = V_{DD} + (V_{CC} - V_{DD}) \frac{R_1}{R_1 + R_2} =$$

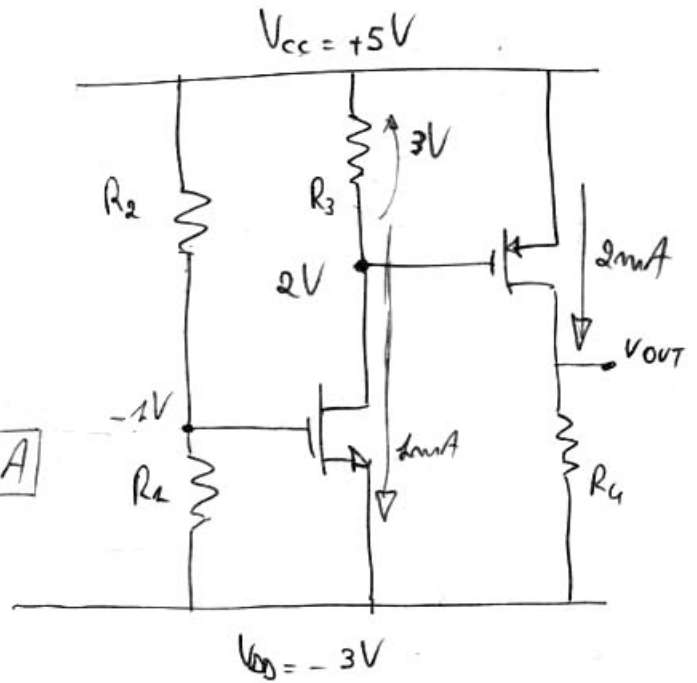
$$= -3V + 8V \times \frac{20k\Omega}{20k\Omega + 60k\Omega} = \boxed{-1V}$$

$$\boxed{I_{Dn}} = k_n (V_{GSn} - V_{Tn})^2 = \frac{1mA}{V^2} (2V - 1V)^2 = \boxed{1mA}$$

$$\boxed{V_{GSp}} = V_{R3} = I_D R_3 = \boxed{3V}$$

$$\boxed{I_{Dp}} = k_p (V_{GSp} - V_{Tp})^2 = \frac{1}{2} \frac{mA}{V^2} (3V - 1V)^2 = \boxed{2mA}$$

$$\boxed{V_{OUT}} = V_{DD} + R_4 I_{Dp} = -3V + 2mA \times 1.5k\Omega = \boxed{0V}$$



VERIFICA SAT ROS:

N-MOS:

$$V_D = 2V \rightarrow \text{OK! (visto che } V_D > V_G)$$

$$V_G = -1V$$

P-MOS:

$$V_D = 0V \rightarrow \text{OK! (perche } V_D < V_G)$$

$$V_G = 2V$$

$$\boxed{g_{mn}} = \frac{2I_D}{(V_{GSn} - V_{Tn})} = \frac{2mA}{2V - 1V} = \boxed{\frac{2mA}{V}} \rightarrow \frac{1}{g_{mn}} = 500\Omega$$

$$\boxed{g_{mp}} = \frac{2I_D}{(V_{GSp} - V_{Tp})} = \frac{4mA}{(3V - 1V)} = \boxed{\frac{2mA}{V}} \rightarrow \frac{1}{g_{mp}} = 500\Omega$$

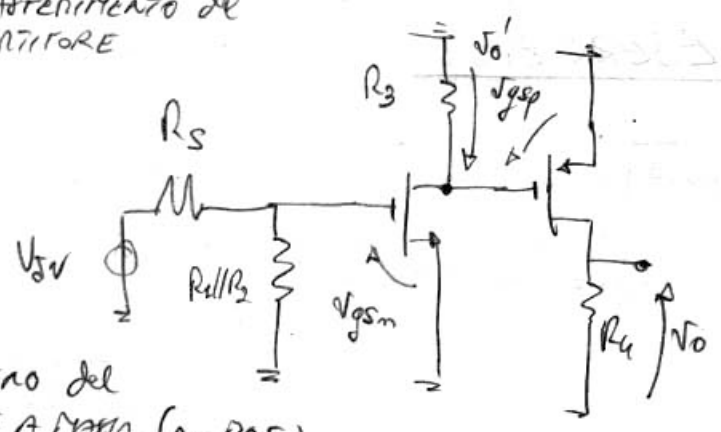
3.B)

$$v_{gs} = \frac{R_1 // R_2}{R_1 // R_2 + R_s} v_{IV} = T_p v_{IV}$$

TRASFERIMENTO del PARTITORE

$$v_{o'} = v_{gs} (-g_{m1} R_3) = G_m v_{gs}$$

GUADAGNO del SOURCE A MARRA (n-POS)



$$v_o = v_{gs} (-g_{m2} R_4) = G_p v_{gs} = G_p v_{o'}$$

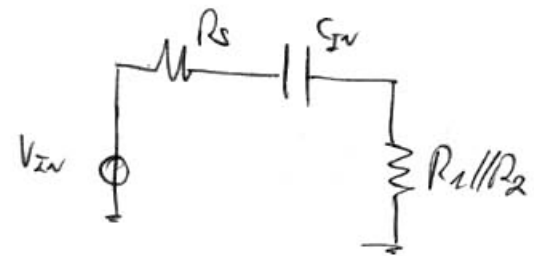
GUADAGNO del SOURCE A MARRA (p-POS)

$$\Rightarrow \overline{|G|} = \frac{v_o}{v_{IV}} = \dots = |T_p \cdot G_m \cdot G_p| = \frac{20k // 60k}{20k // 60k + 5k} \left(-\frac{2mA}{V} \cdot 3k \right) \left(-\frac{2mA}{V} \cdot 1.5k \right)$$

$$= \frac{3}{4} \times (-6) \times (-3V) = \underline{13.5}$$

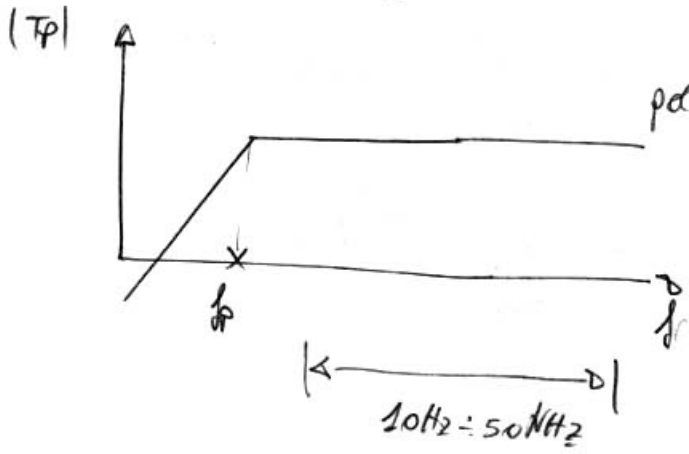
3.C)

Considerando C l'unico tempo del trasferimento che si modifica e T_p .



$$T_p(0) = 0 \quad (\text{CONDENSATORE APERTO})$$

$$T_p(\infty) = \frac{R_1 // R_2}{R_1 // R_2 + R_s}$$



$$p_{do}: \tau_p = C_{IN} (R_s + R_1 // R_2)$$

$$f_p = \frac{1}{2\pi\tau_p}$$

↳ AFFINCHÉ IL CIRCUITO AMPLIFICHI COMETTAIEN TE LA BANDA 10Hz = 50MHz, OCCORRE CHE IL POLO f_p SIA ALMENO UNA DECADA MINA dei 10Hz

$$f_p = 1\text{Hz} \Rightarrow \frac{1}{2\pi C_p} = 1\text{Hz} \Rightarrow \boxed{C_{in}} = \frac{1}{2\pi \cdot 1\text{Hz} \cdot (R_s + R_{s1}/R_L)} = \boxed{27,6 \mu\text{F}}$$

3.D

$$V_{Gp} = 2\text{V}$$

Per rimanere in saturazione, il drain del P-MOS può al massimo arrivare ad una soglia sopra il gate:

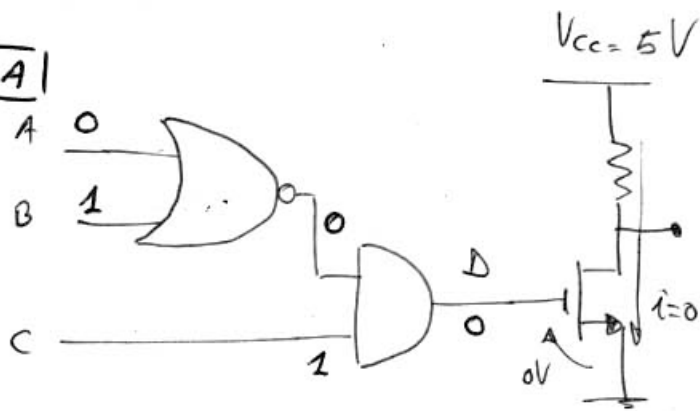
$$V_{D_{max}} = V_{Gp} + |V_{TP}| = 2\text{V} + 1\text{V} = 3\text{V} \quad (\Rightarrow V_{R_{G_{max}}} = V_{D_{max}} - V_{DD} = 6\text{V})$$

Quindi:

$$\boxed{R_{G_{max}}} = \frac{V_{R_{G_{max}}}}{I_{D1}} = \frac{6\text{V}}{2\text{mA}} = \boxed{3\text{k}\Omega}$$

ESERCIZIO 4

4.A

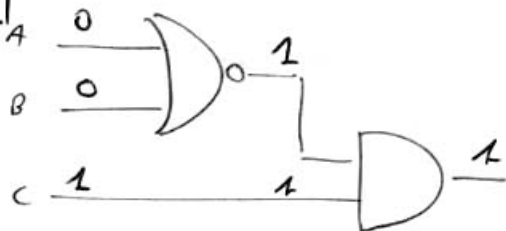


$D = 0$ logico $\rightarrow 0V \rightarrow$

$\rightarrow V_{GS} = 0V \rightarrow$ **MOS OFF**

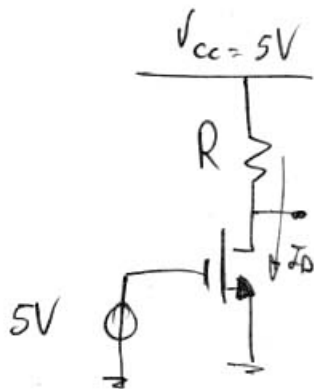
$\rightarrow i_D = 0 \rightarrow$ **$V_{OUT} = V_{CC} = 5V$**

4.B



$D = 1$ logico $\rightarrow 5V \rightarrow$

$\rightarrow V_{GS} = 5V$



IPOTIZZANDO IL MOS IN SATURAZIONE:

$$I_D = K(V_{GS} - V_T)^2 = \frac{200\mu A}{V^2} (5V - 2V)^2 = 900\mu A$$

$$V_{OUT} = V_{CC} - R I_D = 5V - 900\mu A \times 16,5k\Omega =$$

$= -143,5V$ \leftarrow NON È POSSIBILE \Rightarrow IL MOS NON È IN SATURAZIONE, MA IN ZONA OHMICA

$$\begin{cases} I_D = \frac{V_{CC} - V_{OUT}}{R} = \frac{V_{CC} - V_{GS}}{R} \\ I_D = K[2(V_{GS} - V_T)V_{GS} - V_{GS}^2] \end{cases} \Leftrightarrow$$

$$\frac{V_{CC} - V_{GS}}{R} = K[2(V_{GS} - V_T)V_{GS} - V_{GS}^2] \Leftrightarrow$$

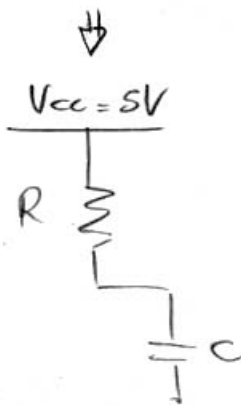
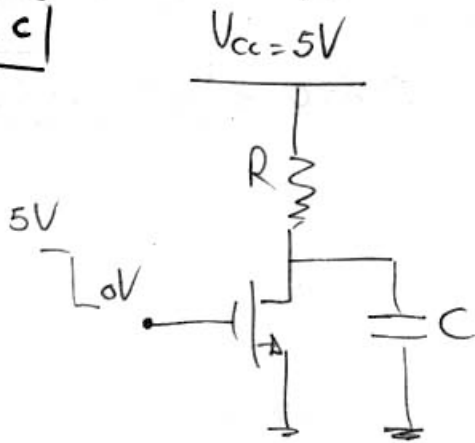
$$(16,5V^{-1}) V_{GS}^2 - 100 V_{GS} + 5V = 0$$

$$V_{GS} = \begin{cases} 6V \rightarrow 10 \times > V_{CC} \\ 59,6mV \end{cases}$$

Quindi

$$V_{out} = 50.4 \text{ mV}$$

4.c



INIZIAMENTE,

$$V_G = 5V \rightarrow \text{MOS ACCESSO} \rightarrow V_{out} = 50.4 \text{ mV}$$

INGRESSO CONDUTA,

IL MOS SI SPENDE → IL CIRCUITO È
RIVEDO AD UN RC

$$V_o(t) = [V_o(\infty) - V_o(0)] (1 - e^{-t/\tau}) + V_o(0) \Rightarrow$$

$$\tau = -\tau \ln \frac{V_o(\infty) - V_o(t)}{V_o(\infty) - V_o(0)}$$

50% dell'oscillazione,
quindi:

$$V_o(0) - V_o(t) = 0.5 \times [V_o(\infty) - V_o(0)]$$

$$= -\tau \ln(0.5) = \boxed{0.69 \tau}$$

$$\tau = 0.69 \times R \times C = 0.69 \times 16.5 \text{ k}\Omega \times 100 \text{ pF} =$$

$$= \boxed{113.85 \text{ ms}}$$